



XA-9574

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Hideki YASUOKA et al.

Appln. No.: 09/989,061

Group Art Unit: 2812

Filed: November 21, 2001

Examiner: J. Kennedy

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND METHOD
OF MANUFACTURING THE SAME

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AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

In response to the Office Action mailed February 4,
2003, please amend the above-identified patent application
as indicated below:

Amendments to the specification begin on page 2 of
this paper.

Amendments to the claims are reflected in the listing
of claims which begins on page 18 of this paper.

Remarks/Arguments begin on page 45 of this paper.